# BAT54XV2T1G

# **Schottky Barrier Diodes**

These Schottky barrier diodes are designed for high-speed switching applications, circuit protection, and voltage clamping. Extremely low forward voltage reduces conduction loss. Miniature surface mount package is excellent for hand-held and portable applications where space is limited.

### Features

Junction-to-Ambient

• Extremely Fast Switching Speed

- Low Forward Voltage 0.35 V (Typ) @  $I_F = 10 \text{ mA}$
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant



## **ON Semiconductor®**

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# **30 VOLT** SILICON HOT-CARRIER DETECTOR AND SWITCHING DIODES

-0 2

<b>MAXIMUM RATINGS</b> (T <sub>J</sub> = $125^{\circ}C$ unless	CATHODE ANODE			
Rating	Symbol	Value	Unit	
Reverse Voltage	V <sub>R</sub>	30	V	
THERMAL CHARACTERISTICS				2 SOD-523 CASE 502
Characteristic	Symbol	Max	Unit	1 PLASTIC
Total Device Dissipation FR-5 Board,	PD	200	mW	
(Note 1) T <sub>A</sub> = 25°C Derate above 25°C		1.57	mW/°C	MARKING DIAGRAM
Forward Current (DC)	١ <sub>F</sub>	200 Max	mA	
Non-Repetitive Peak Forward Current, t <sub>p</sub> < 10 msec	I <sub>FSM</sub>	600	mA	
Repetitive Peak Forward Current Pulse Wave = 1 sec, Duty Cycle = 66%	I <sub>FRM</sub>	300	mA	JV = Device Code
Thermal Resistance,	$R_{\theta JA}$	635	°C/W	JV = Device Code M – Date Code*

°C

Junction and Storage Temperature T<sub>J</sub>, T<sub>stg</sub> -55 to 125 Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. FR-4 Minimum Pad.

### IAGRAM



e Code = Date Code\* IVI = Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation may vary depending upon manufacturing location.

### **ORDERING INFORMATION**

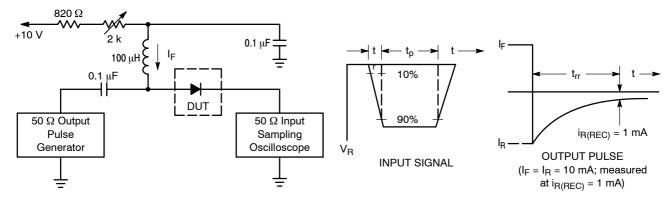
Device	Package	Shipping <sup>†</sup>
BAT54XV2T1G	SOD–523 (Pb–Free)	3000 / Tape & Reel
BAT54XV2T5G	SOD-523 (Pb-Free)	8000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# BAT54XV2T1G

### **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Breakdown Voltage (I <sub>R</sub> = 10 μA)	V <sub>(BR)R</sub>	30	_	_	V
Total Capacitance (V <sub>R</sub> = 1.0 V, f = 1.0 MHz)	CT	-	7.6	10	pF
Reverse Leakage (V <sub>R</sub> = 25 V)	۱ <sub>R</sub>	-	0.5	2.0	μΑ
Forward Voltage (I <sub>F</sub> = 0.1 mA)	V <sub>F</sub>	-	0.22	0.24	V
Forward Voltage (I <sub>F</sub> = 1.0 mA)	V <sub>F</sub>	-	0.29	0.32	V
Forward Voltage (I <sub>F</sub> = 10 mA)	V <sub>F</sub>	-	0.35	0.40	V
Forward Voltage (I <sub>F</sub> = 30 mA)	V <sub>F</sub>	-	0.41	0.5	V
Forward Voltage (I <sub>F</sub> = 100 mA)	V <sub>F</sub>	-	0.52	0.8	V
Reverse Recovery Time ( $I_F = I_R = 10 \text{ mA}, I_{R(REC)} = 1.0 \text{ mA}$ ) Figure 1	t <sub>rr</sub>	-	-	5.0	ns

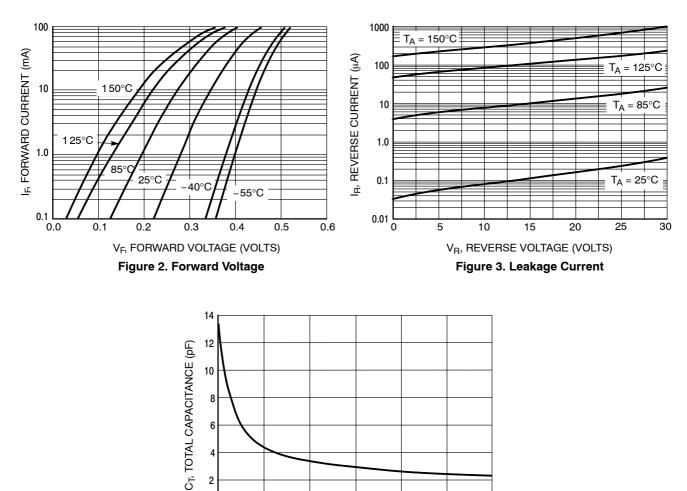


Notes: 1. A 2.0 k $\Omega$  variable resistor adjusted for a Forward Current (I<sub>F</sub>) of 10 mA. 2. Input pulse is adjusted so I<sub>R(peak)</sub> is equal to 10 mA.

3. t<sub>p</sub> » t<sub>rr</sub>

Figure 1. Recovery Time Equivalent Test Circuit

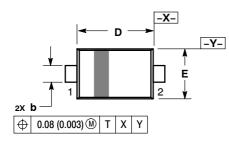
# BAT54XV2T1G

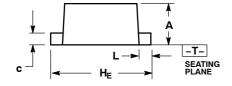


V<sub>R</sub>, REVERSE VOLTAGE (VOLTS) Figure 4. Total Capacitance

### PACKAGE DIMENSIONS

SOD-523 CASE 502-01 ISSUE D





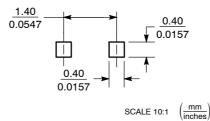
NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

2. CONTROLLING DIMENSION: MILLIMETER.

 MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.50	0.60	0.70	0.020	0.024	0.028	
b	0.25	0.30	0.35	0.010	0.012	0.014	
С	0.07	0.14	0.20	0.0028	0.0055	0.0079	
D	1.10	1.20	1.30	0.043	0.047	0.051	
Е	0.70	0.80	0.90	0.028	0.032	0.035	
HE	1.50	1.60	1.70	0.059	0.063	0.067	
Г	0.15	0.20	0.25	0.006	0.008	0.010	

#### SOLDERING FOOTPRINT\*



\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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